

FIG

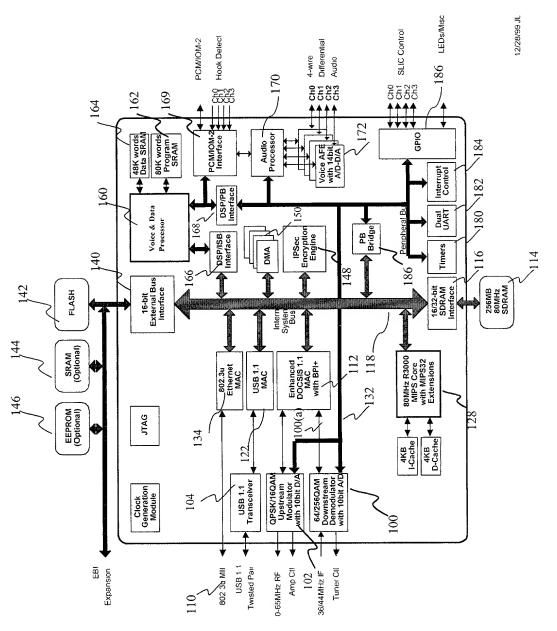


FIG. 3

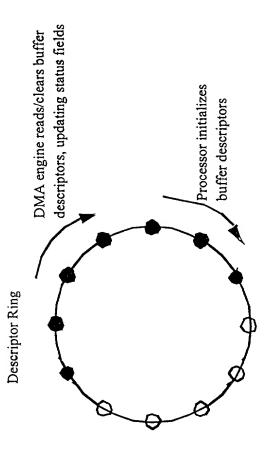
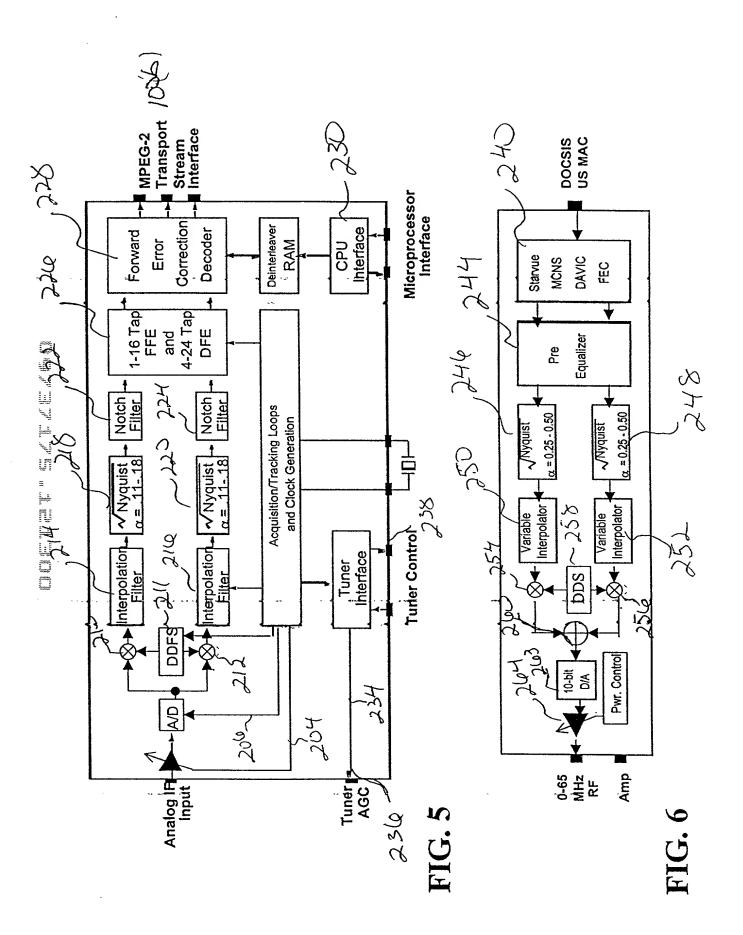


FIG. 4



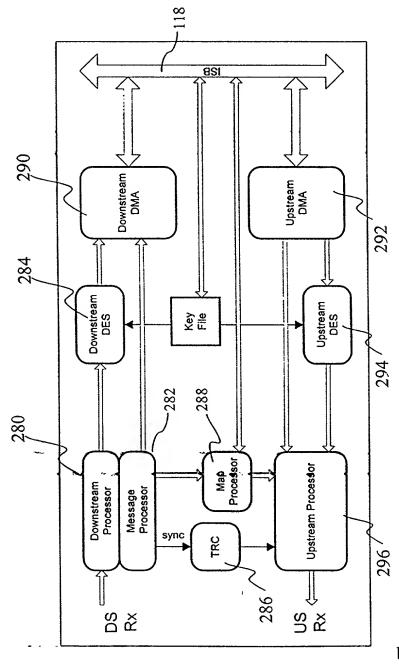


FIG.

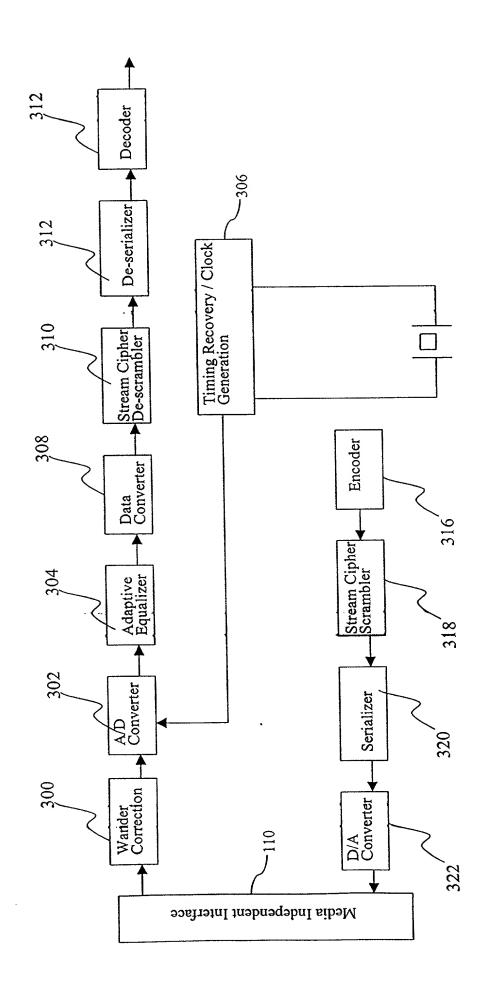


FIG. 8

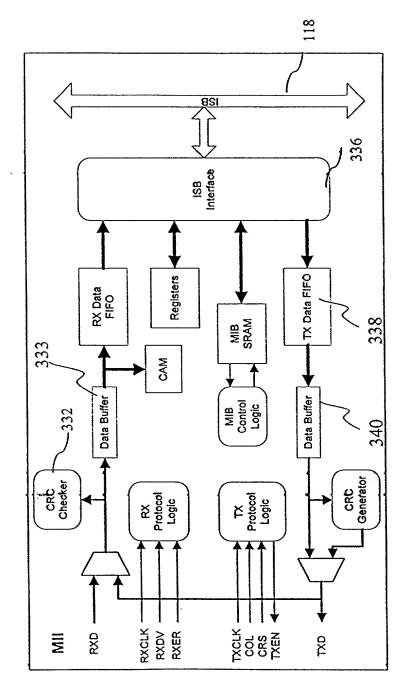
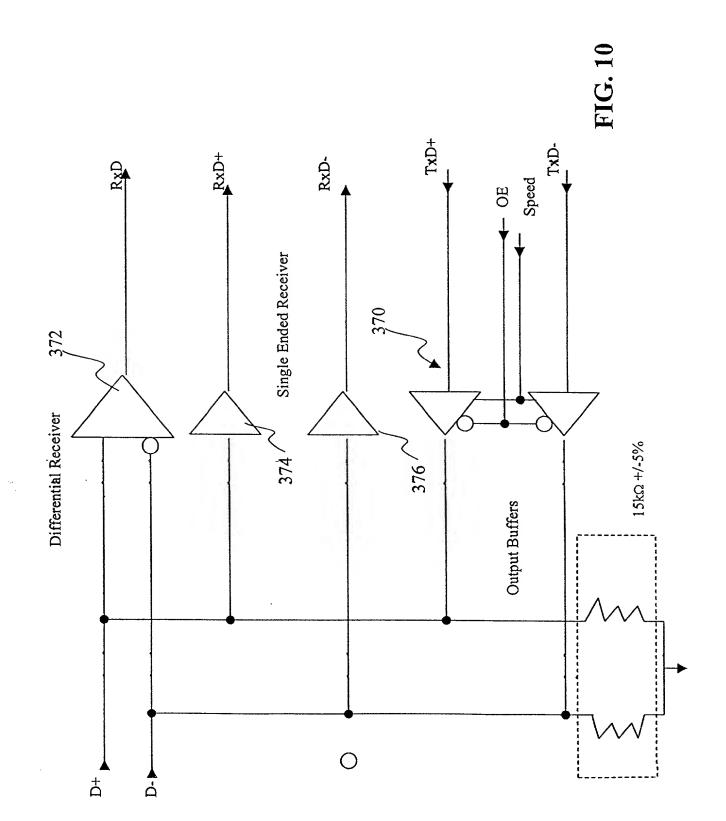


FIG. 9



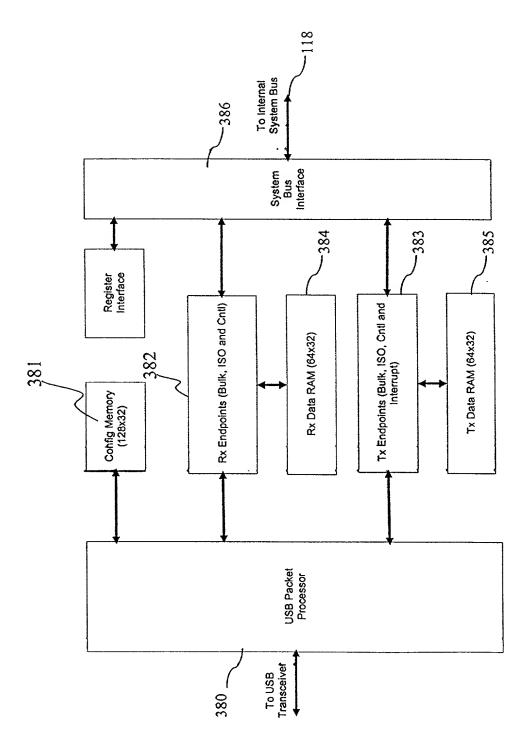
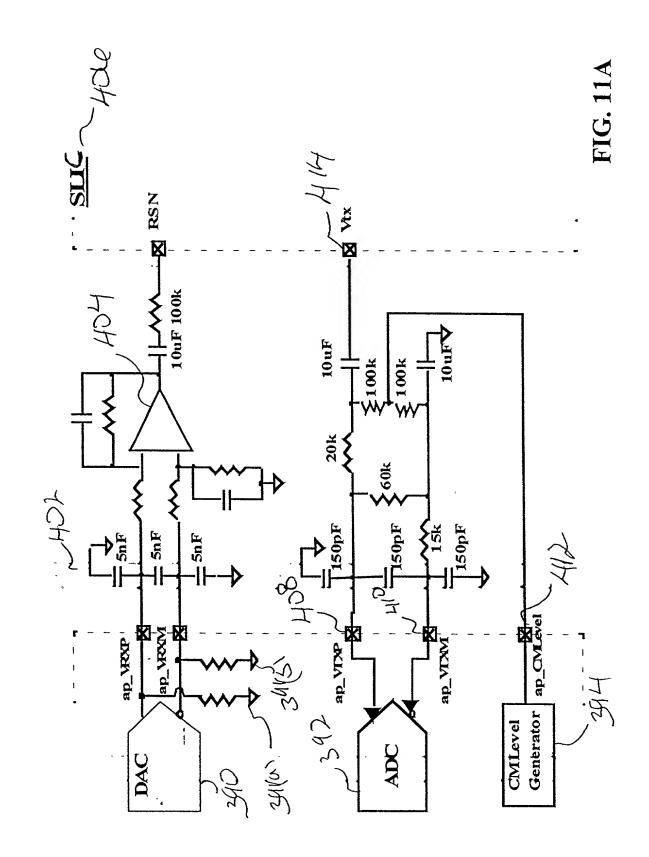
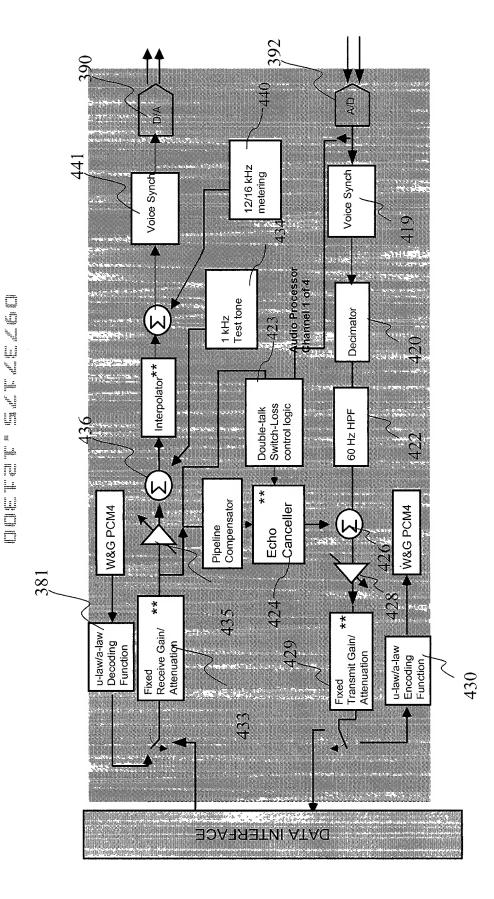
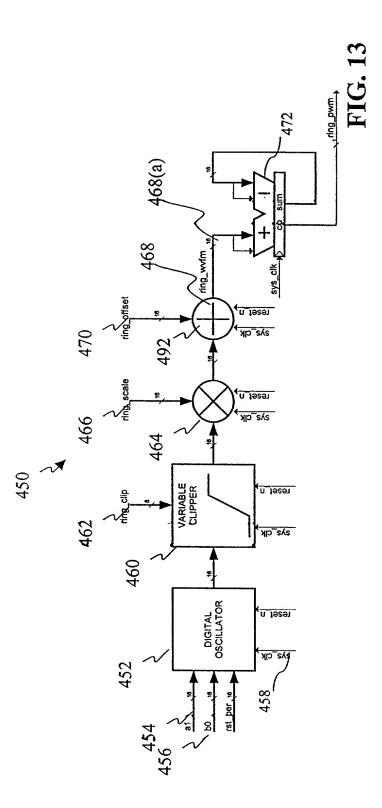
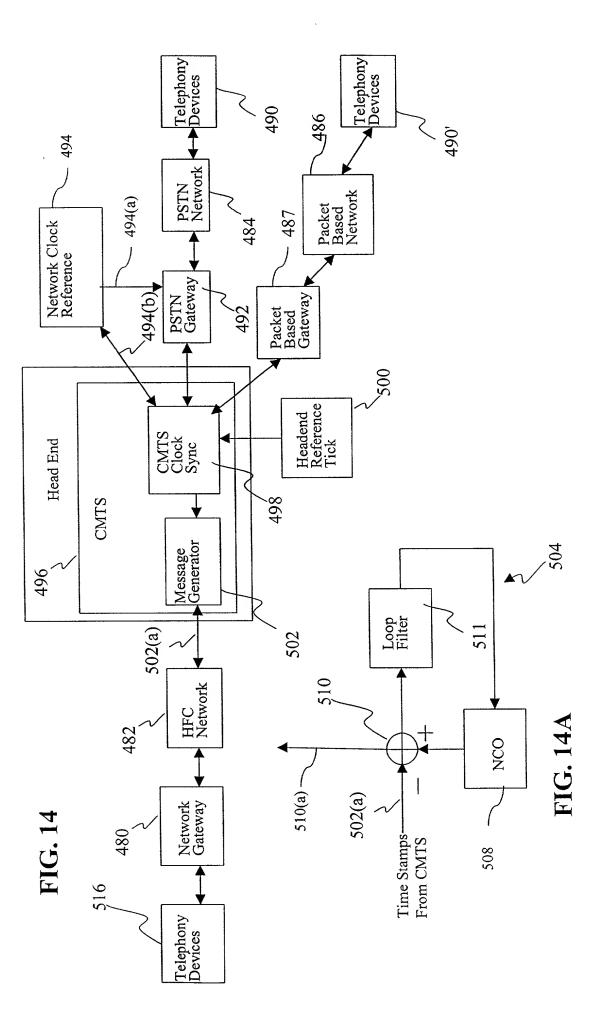


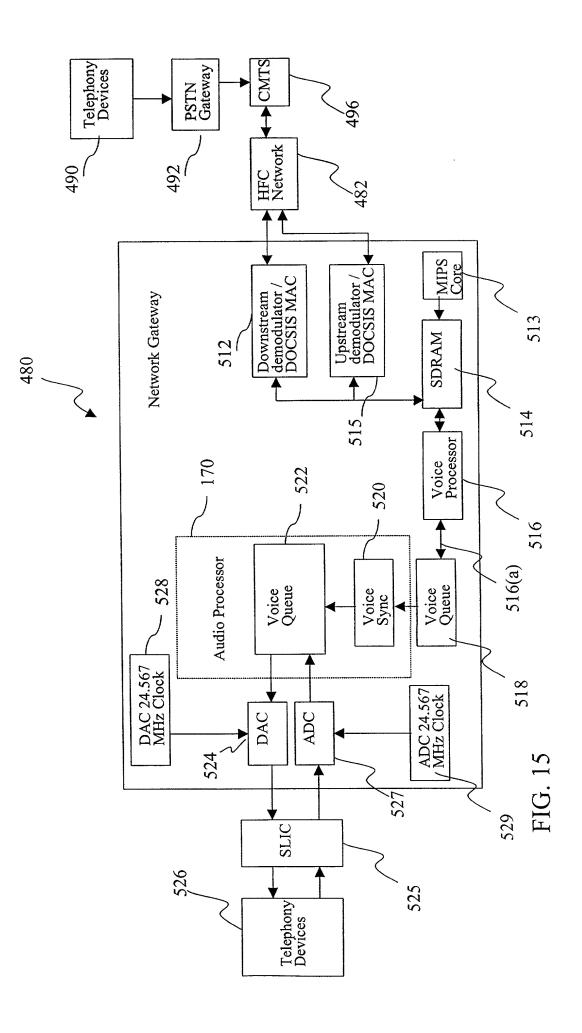
FIG. 10A











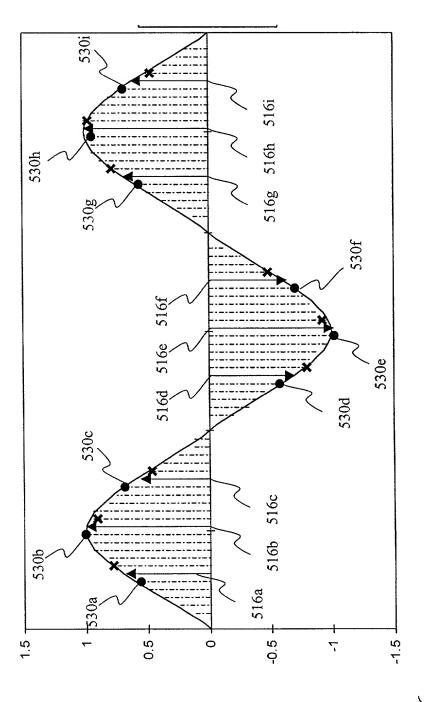
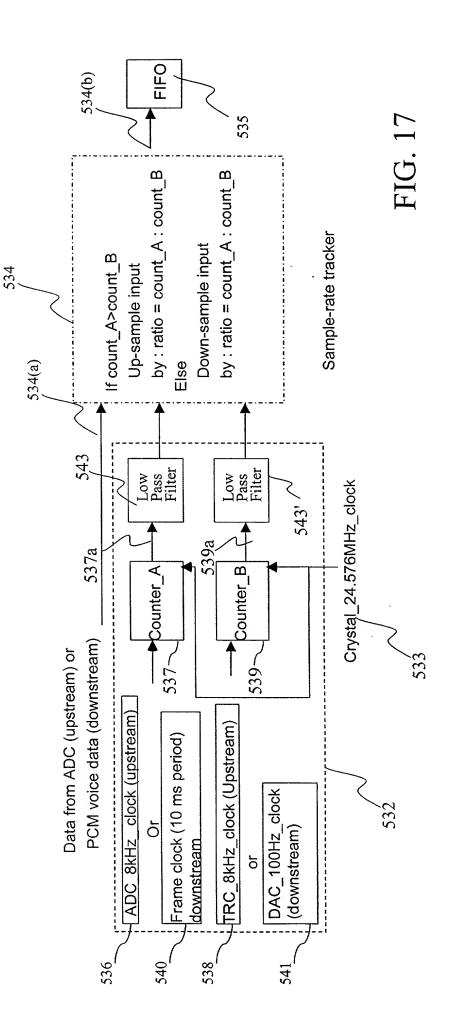


FIG. 16



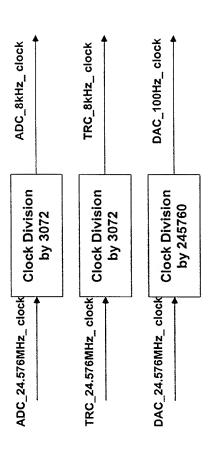
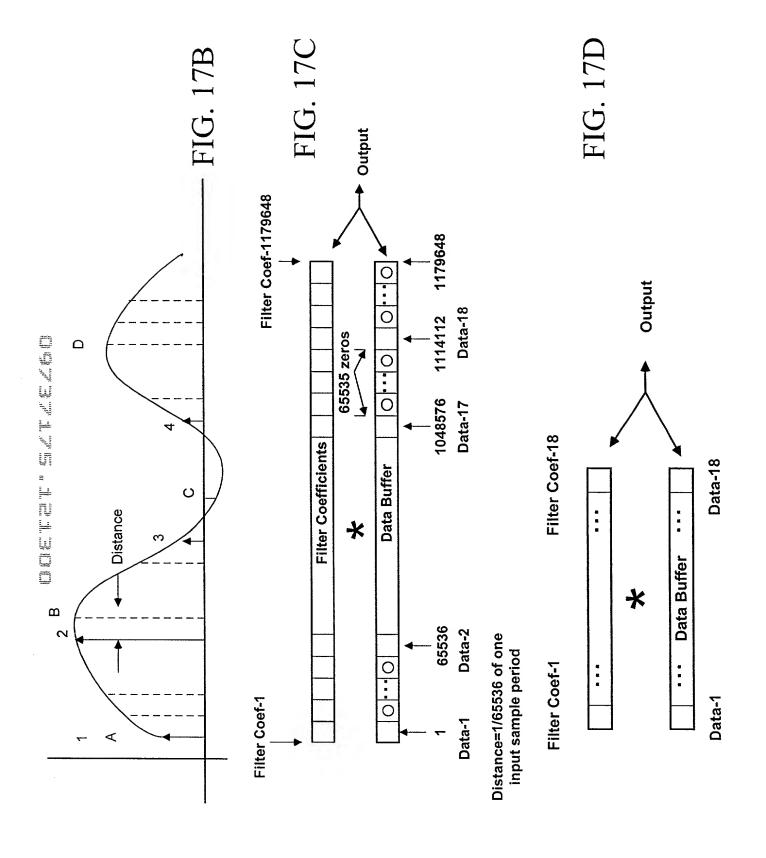


FIG. 17A



480

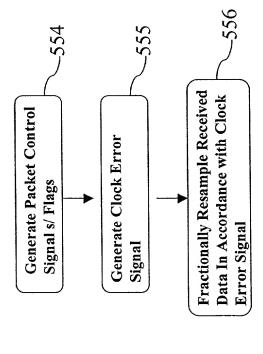
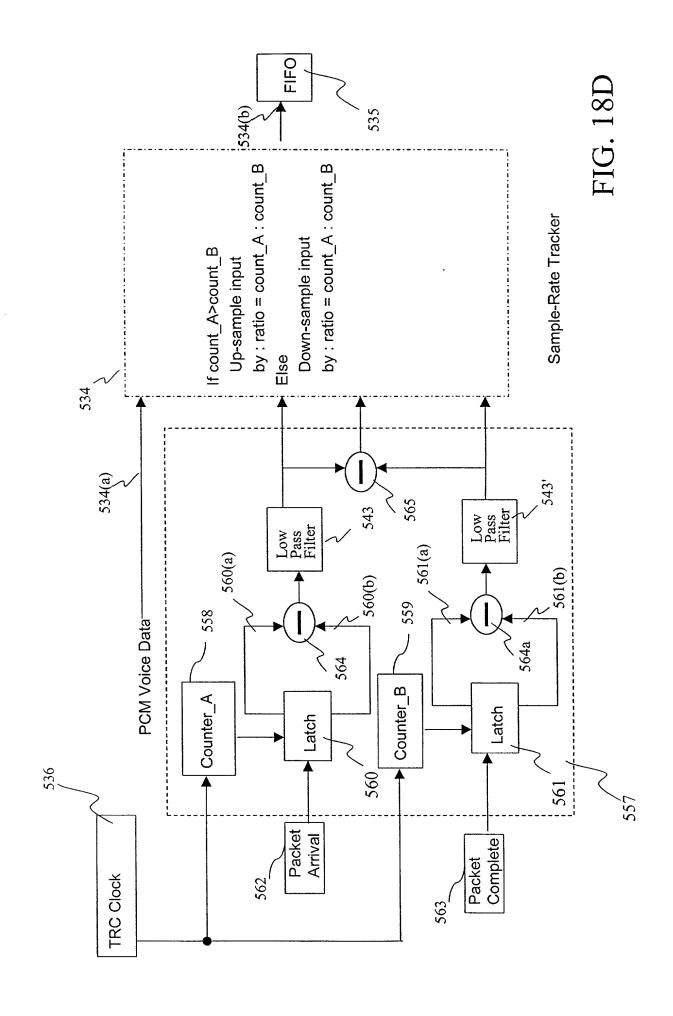


FIG. 18C



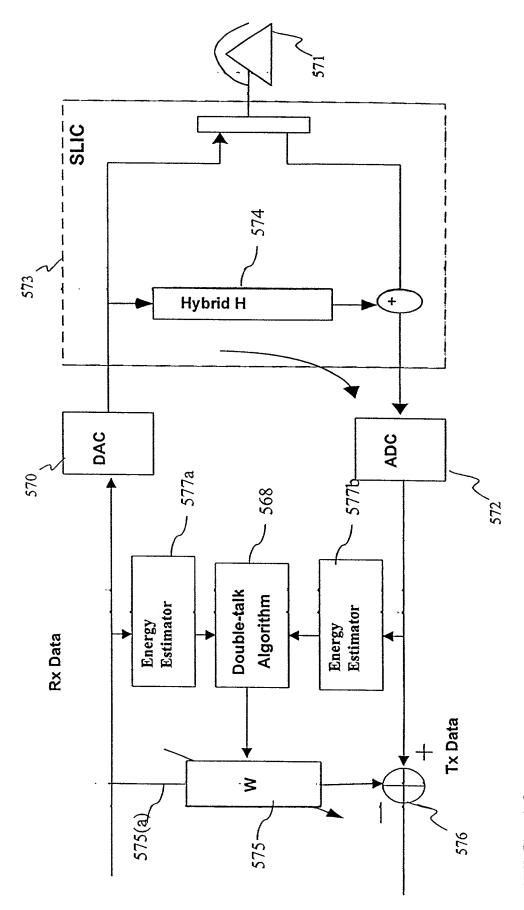


FIG. 19

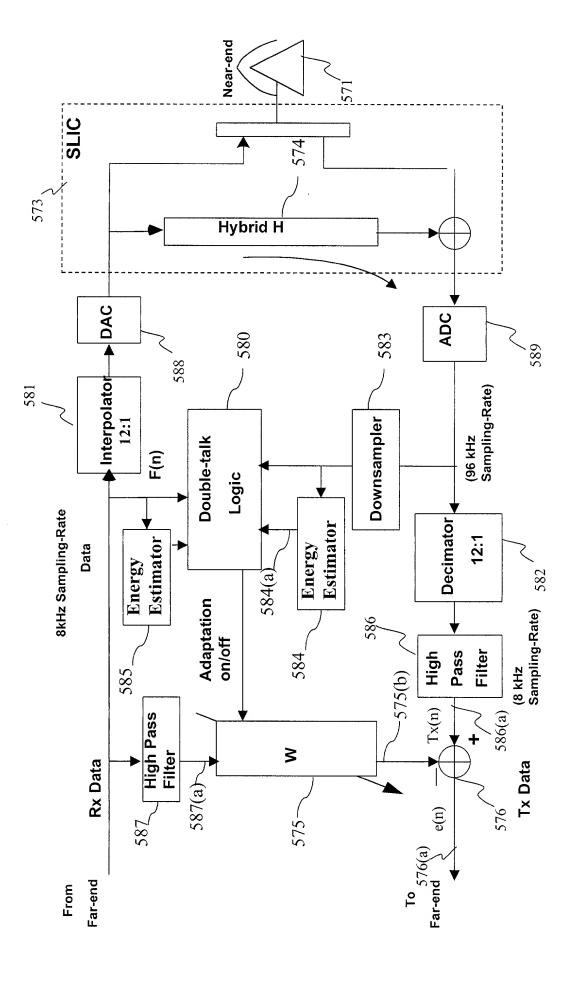


FIG. 20

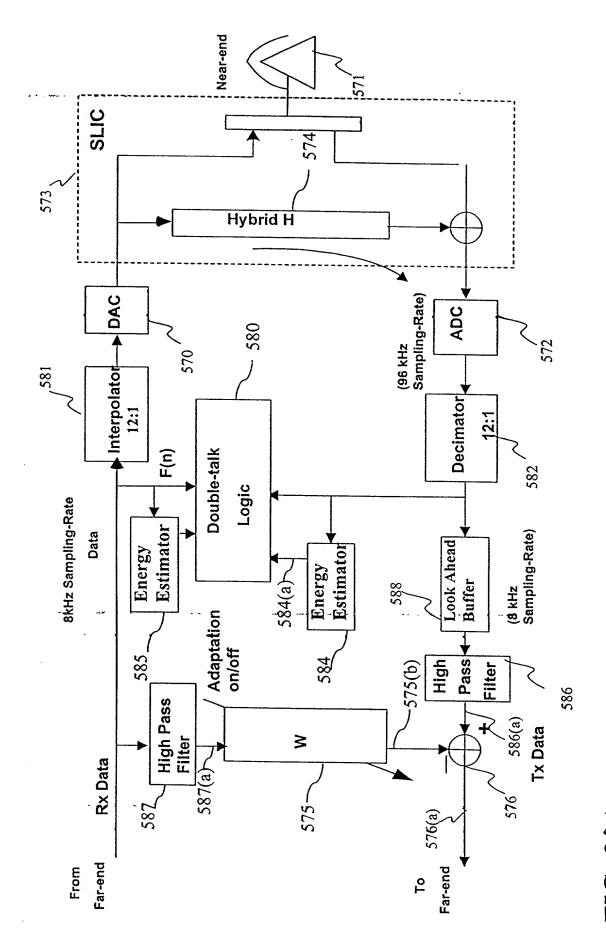
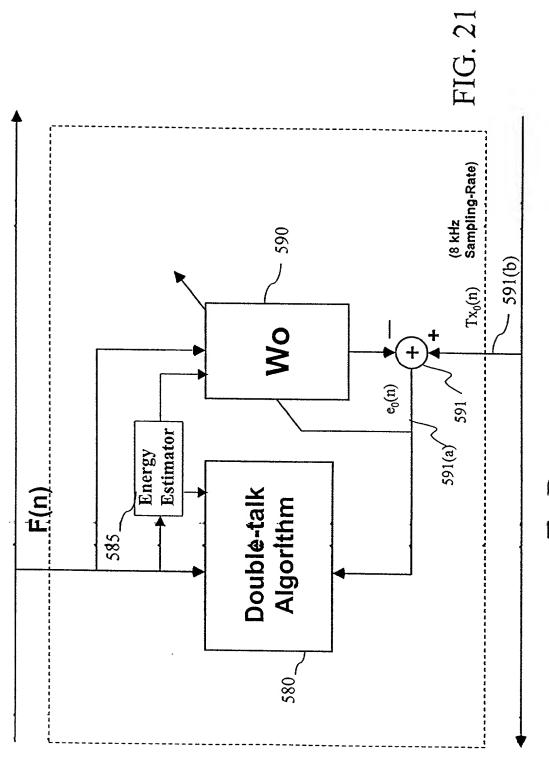
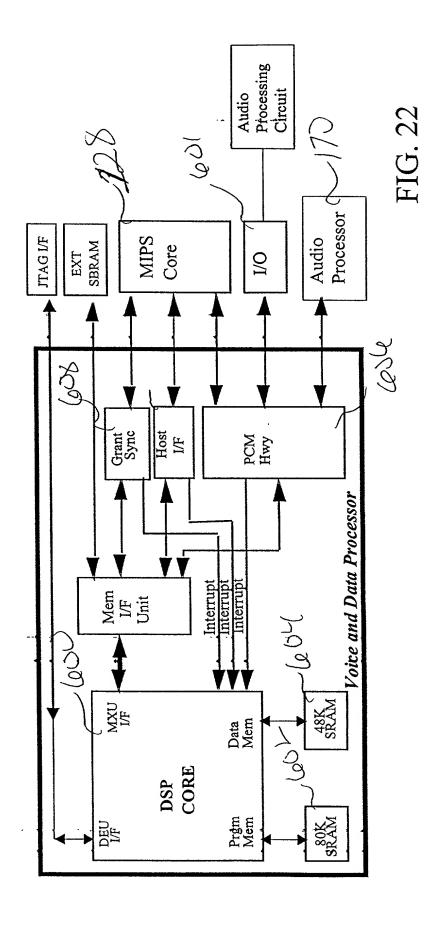
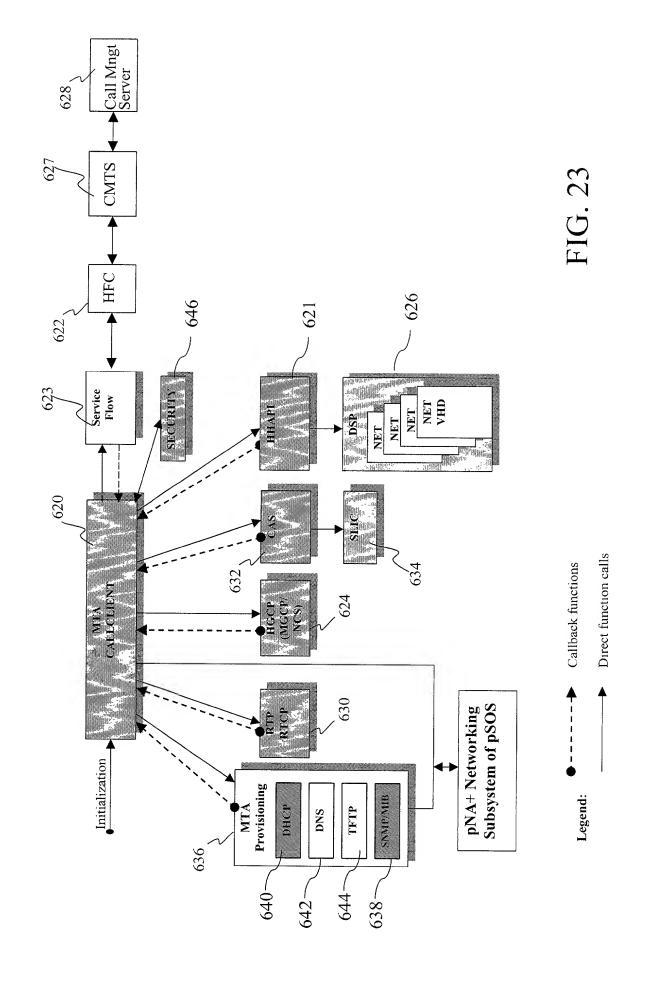


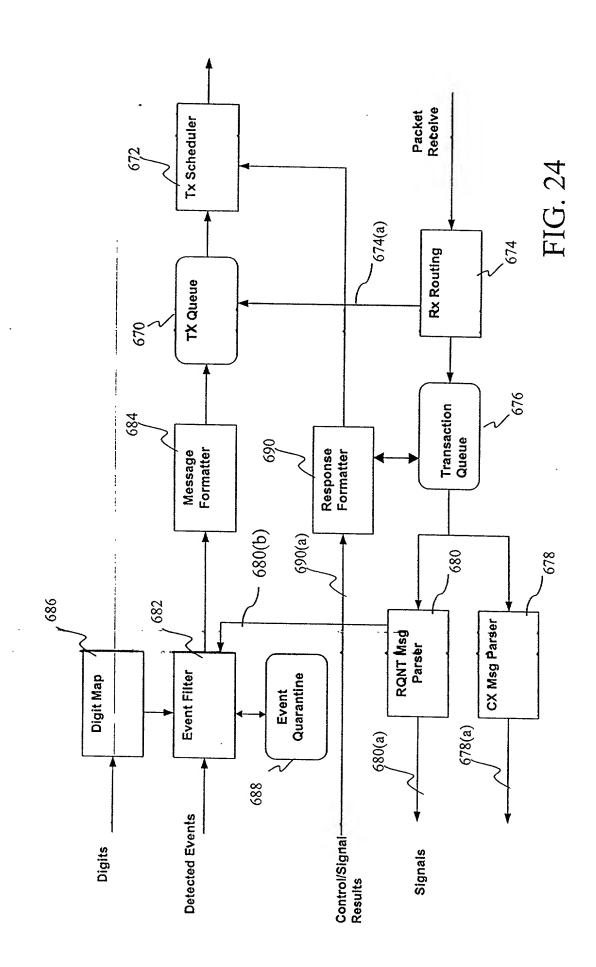
FIG. 20A



Tx Data







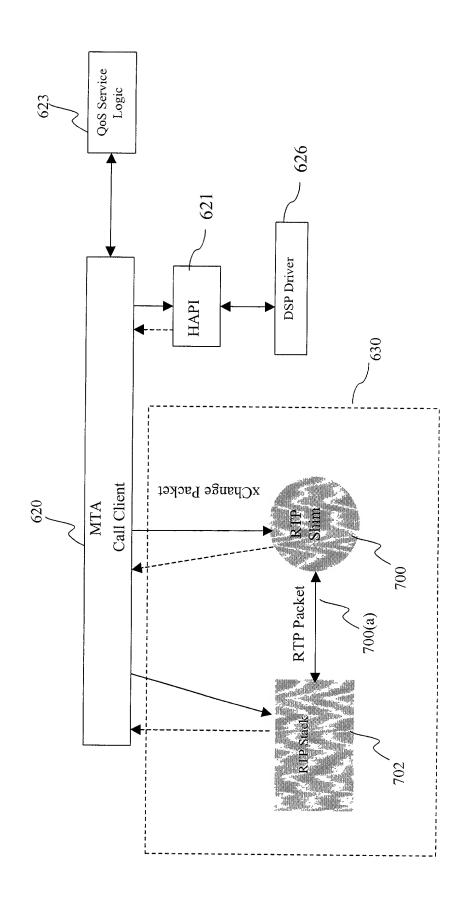


FIG. 24

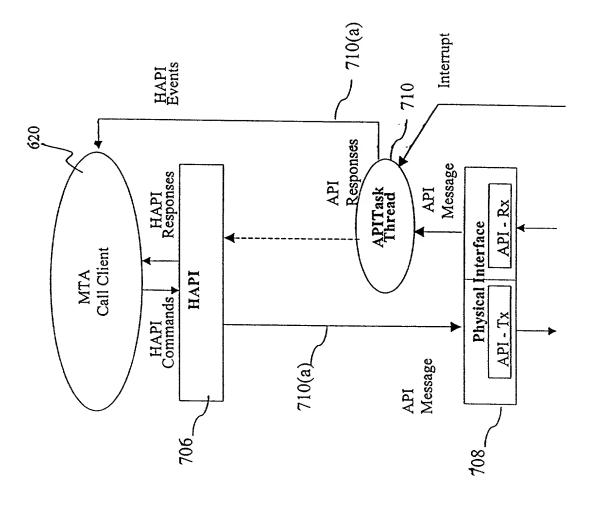
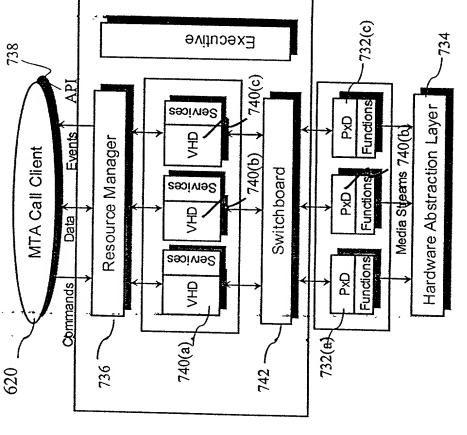


FIG. 26

FIG. 27



To Underlying Hardware

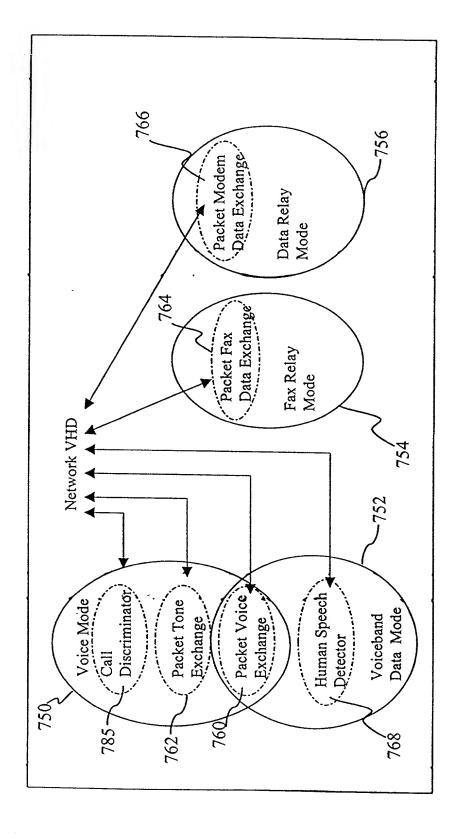
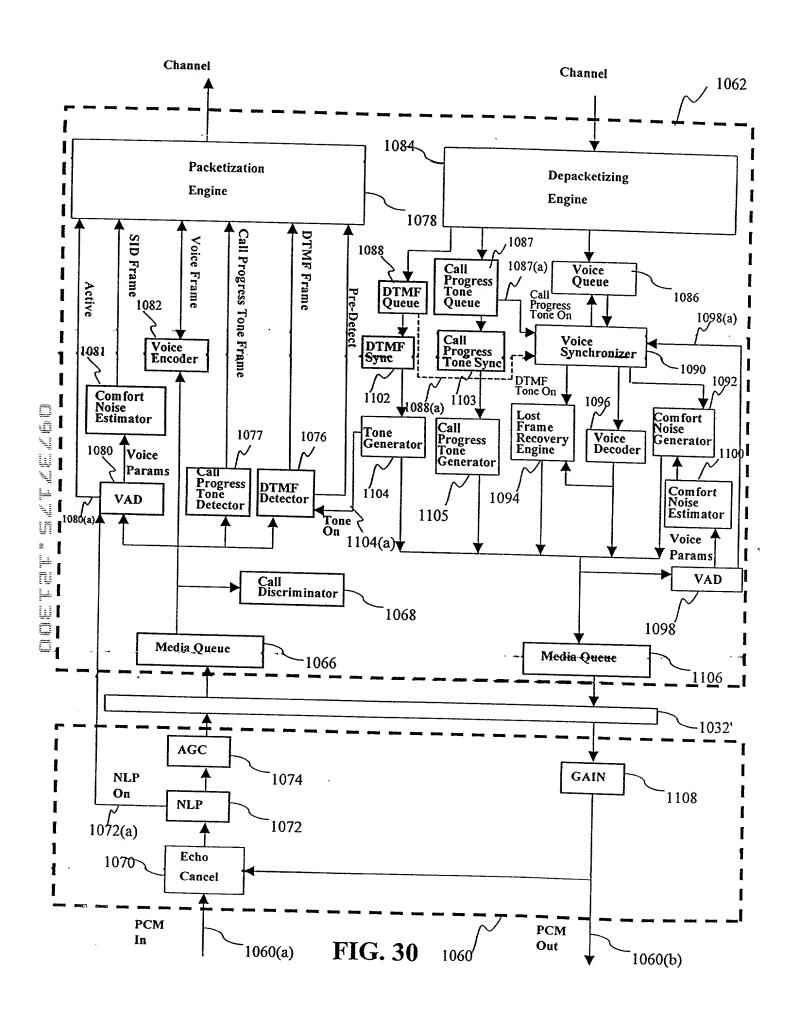


FIG. 29



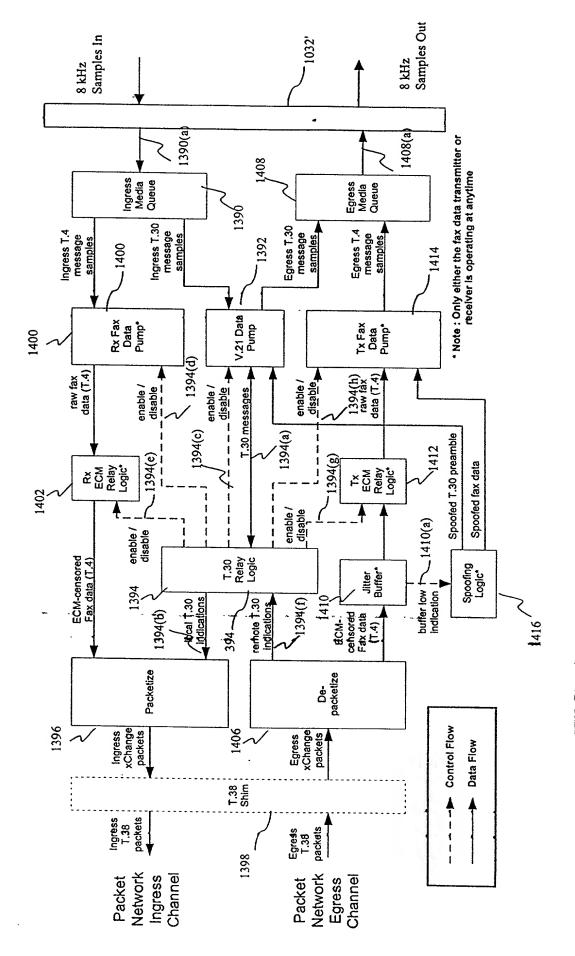


FIG. 31

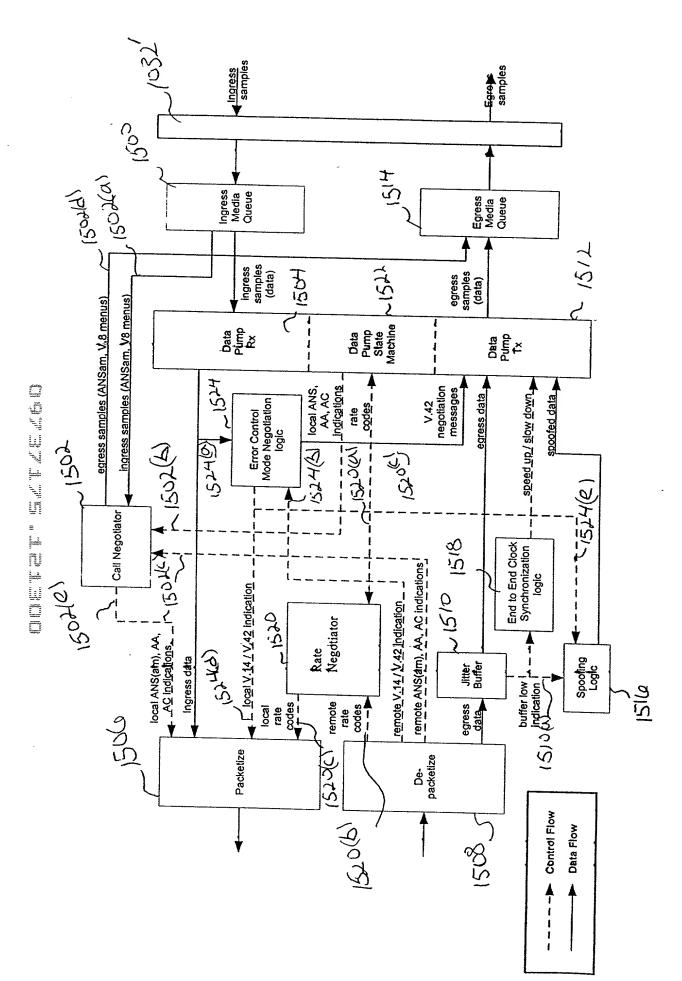


FIG. 32